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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete If Known	
Application Number	10/666,397
Filing Date	September 18, 2003
First Named Inventor	Richard Nicholson
Art Unit	2878
Examiner Name	
Attorney Docket Number	55123P275

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	Jeffrey Zweizig	Date Considered	9/15/04
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Based on PTO/SB/08A (08-03) as modified by Blakely, Schickoff, Taylor & Zafman (wtr) 08/11/2003

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JZ		DICKSON, JOHN F., "On-Chip High-Voltage Generation in MNOS Integrated Circuits Using an Improved Voltage Multiplier Technique", IEEE Journal of Solid-State Circuits, June 1976, Vol. SC-11, No. 3, Pgs. 374-378	

Examiner Signature	Jeffrey Zweizig	Date Considered	9/15/04
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